|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 |  |
| r3 |  |

|  |  |
| --- | --- |
| 4MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 00  C D tag | 100 |
| 110 |
| 0  C D tag |  |
|  |
| 1  C D tag |  |
|  |
| 1  C D tag |  |
|  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag | 100 |
| 180 |
| 0 0 10  C D tag  (LRU) | 180 |
| 190 |
| 1  C D tag |  |
|  |
| 1  C D tag |  |
|  |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 | 180 |
| r3 |  |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 | 180 |
| r3 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 00  C D tag  (LRU) | 100 |
| 110 |
| 0 0 10  C D tag | 180 |
| 190 |
| 1  C D tag |  |
|  |
| 1  C D tag |  |
|  |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
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| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag | 100 |
| 180 |
| 0 0 10  C D tag  (LRU) | 180 |
| 190 |
| 1 1 01  C D tag | 160 |
| 180 |
| 1  C D tag |  |
|  |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 | 180 |
| r3 |  |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag  (LRU) | 100 |
| 180 |
| 0 0 11  C D tag | 220 |
| 230 |
| 1 1 01  C D tag  (LRU) | 160 |
| 180 |
| 1 0 00  C D tag | 120 |
| 130 |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 220 |
| r1 | 120 |
| r2 | 180 |
| r3 |  |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

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lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 220 |
| r1 |  |
| r2 | 180 |
| r3 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag  (LRU) | 100 |
| 180 |
| 0 0 11  C D tag | 220 |
| 230 |
| 1 1 01  C D tag | 160 |
| 180 |
| 1  C D tag |  |
|  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 01  C D tag | 220 |
| 150 |
| 0 0 11  C D tag  (LRU) | 220 |
| 230 |
| 1 1 01  C D tag  (LRU) | 160 |
| 180 |
| 1 0 00  C D tag | 120 |
| 130 |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 220 |
| r1 | 120 |
| r2 | 180 |
| r3 |  |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 120 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
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| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 180 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
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| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 180 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
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| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 180 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 01  C D tag  (LRU) | 220 |
| 150 |
| 0 0 10  C D tag | 180 |
| 190 |
| 1 1 01  C D tag  (LRU) | 160 |
| 180 |
| 1 0 00  C D tag | 120 |
| 130 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 01  C D tag | 220 |
| 150 |
| 0 0 11  C D tag  (LRU) | 220 |
| 230 |
| 1 1 01  C D tag  (LRU) | 160 |
| 180 |
| 1 0 00  C D tag | 120 |
| 130 |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 220 |
| r1 | 120 |
| r2 | 130 |
| r3 | 190 |

PROCESSADOR

lw r0 0

lw r2 8

sw r2 1

sw r2 7

lw r0 12

lw r1 2

sw r0 4

lw r2 3

lw r3 9

|  |  |
| --- | --- |
| r0 | 220 |
| r1 | 120 |
| r2 | 130 |
| r3 |  |